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The graphics processor (GPU) on today's commodity video cards has evolved into an extremely powerful and flexible processor. The latest graphics architectures provide tremendous memory bandwidth and computational horsepower, with fully programmable vertex ...

2 [Delivering acceleration: the potential for increased HPC application performance using reconfigurable logic](#)

[David Caliga](#), [David Peter Barker](#)

November 2001 **Supercomputing '01: Proceedings of the 2001 ACM/IEEE conference on Supercomputing 2001** (CDROM)

**Publisher:** ACM

Full text available: Pdf (396.61 KB)

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SRC Computers, Inc. has integrated adaptive computing into its SRC-6 high-end server, incorporating reconfigurable processors as peers to the microprocessors. Performance improvements resulting from reconfigurable computing can provide orders of magnitude ...

**Keywords:** FPGA, reconfigurable computing

3 [System-level power optimization: techniques and tools](#)

[Luca Benini](#), [Giovanni de Micheli](#)

April 2000 **Transactions on Design Automation of Electronic Systems (TODAES)**, Volume 5 Issue 2

**Publisher:** ACM [Request Permissions](#)

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**Bibliometrics:** Downloads (6 Weeks): 37, Downloads (12 Months): 565, Downloads (Overall): 4985, Citation Count: 82

This tutorial surveys design methods for energy-efficient system-level design. We consider electronic systems consisting of a hardware platform and software layers. We consider the three major constituents of hardware that consume energy, namely computation, ...

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XML is widely praised for its flexibility in allowing repeated and missing sub-elements. However, this flexibility makes it challenging to develop a bulk algebra, which typically manipulates sets of objects with identical structure. A set of XML elements, ...

#### 5 [Field-testing IMPACT EPIC research results in Itanium 2](#)



[John W. Sias, Sain-zee Ueng, Geoff A. Kent, Ian M. Steiner, Erik M. Nystrom, Wen-mei W. Hwu](#)

June 2004 **ISCA '04**: Proceedings of the 31st annual international symposium on Computer architecture

**Publisher:** ACM

Full text available: Pdf (273.71 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#)

**Bibliometrics:** Downloads (6 Weeks): 5, Downloads (12 Months): 42, Downloads (Overall): 447, Citation Count: 7

Explicitly-Parallel Instruction Computing (EPIC) provides architectural features, including predication and explicit control speculation, intended to enhance the compiler's ability to expose instruction-level parallelism (ILP) in control-intensive programs. ...

Also published in:

March 2004 **SIGARCH Computer Architecture News** Volume 32 Issue 2

#### 6 [Parallel programming models for a multi-processor SoC platform applied to high-speed traffic management](#)



[Pierre G. Paulin, Chuck Pilkington, Michel Langevin, Essaid Bensoudane, Gabriela Nicolescu](#)

September 2004 **CODES+ISSS '04**: Proceedings of the 2nd IEEE/ACM/IFIP international conference on Hardware/software codesign and system synthesis

**Publisher:** ACM

Full text available: Pdf (238.83 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#)

**Bibliometrics:** Downloads (6 Weeks): 4, Downloads (12 Months): 93, Downloads (Overall): 931, Citation Count: 11

In this paper, we describe the MultiFlex multi-processor SoC programming environment, with focus on two programming models: a distributed system object component (DSOC) message passing model, and a symmetrical multi-processing (SMP) model using shared ...

**Keywords:** embedded software, multi-processor systems, system-on-chip

#### 7 [Fast detection of communication patterns in distributed executions](#)

[Thomas Kurz, Michiel F. H. Seuren](#)

November 1997 **CASCON '97**: Proceedings of the 1997 conference of the Centre for Advanced Studies on Collaborative research

**Publisher:** IBM Press

Full text available: Pdf (4.21 MB) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

**Bibliometrics:** Downloads (6 Weeks): 26, Downloads (12 Months): 268, Downloads (Overall): 5943, Citation Count: 0

Understanding distributed applications is a tedious and difficult task. Visualizations based on process-time diagrams are often used to obtain a better understanding of the execution of the application. The visualization tool we use is Poet, an event ...

**8** [The elements of nature: interactive and realistic techniques](#)



[Oliver Deussen](#), [David S. Ebert](#), [Ron Fedkiw](#), [F. Kenton Musgrave](#), [Przemyslaw Prusinkiewicz](#), [Doug Roble](#), [Jos Stam](#), [Jerry Tessendorf](#)

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**Bibliometrics:** Downloads (6 Weeks): 111, Downloads (12 Months): 1332, Downloads (Overall): 7521, Citation Count: 2

This updated course on simulating natural phenomena will cover the latest research and production techniques for simulating most of the elements of nature. The presenters will provide movie production, interactive simulation, and research perspectives ...

**9** [Wormhole IP over \(connectionless\) ATM](#)

[Manolis G. H. Katevenis](#), [Iakovos Mavroidis](#), [Georgios Sapounizis](#), [Eva Kalyvianaki](#), [Ioannis Mavroidis](#), [Georgios Glykopoulos](#)

October 2001 **IEEE/ ACM Transactions on Networking (TON)** , Volume 9 Issue 5

**Publisher:** IEEE Press

Full text available: Pdf (211.25 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#)

**Bibliometrics:** Downloads (6 Weeks): 4, Downloads (12 Months): 21, Downloads (Overall): 346, Citation Count: 1

High-speed switches and routers internally operate using fixed-size cells or segments; variable-size packets are segmented and later reassembled. Connectionless ATM was proposed to quickly carry IP packets segmented into cells (AAL5) using a number of ...

**Keywords:** Connectionless ATM, IP over ATM, gigabit router, routing filter, wormhole IP, wormhole routing

**10** [Real-time volume graphics](#)



[Klaus Engel](#), [Markus Hadwiger](#), [Joe M. Kniss](#), [Aaron E. Lefohn](#), [Christof Rezk Salama](#), [Daniel Weiskopf](#)

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Additional Information: [full citation](#), [abstract](#)

**Bibliometrics:** Downloads (6 Weeks): 44, Downloads (12 Months): 570, Downloads (Overall): 3028, Citation Count: 4

The tremendous evolution of programmable graphics hardware has made high-quality real-time volume graphics a reality. In addition to the traditional application of rendering volume data in scientific visualization, the interest in applying these techniques ...

**11** [Crowd and group animation](#)



[Daniel Thalmann](#), [Christophe Hery](#), [Seth Lippman](#), [Hiromi Ono](#), [Stephen Regeious](#), [Douglas Sutton](#)

August 2004 **SIGGRAPH '04: SIGGRAPH 2004 Course Notes**

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Full text available: Pdf (20.19 MB)

Additional Information: [full citation](#), [abstract](#)

**Bibliometrics:** Downloads (6 Weeks): 44, Downloads (12 Months): 518, Downloads (Overall): 3435, Citation Count: 1

A continuous challenge for special effects in movies is the production of realistic virtual crowds, in terms of rendering and behavior. This course will present state-of-the-art techniques and methods. The course will explain in details the different ...

## 12 [A DISE implementation of dynamic code decompression](#)



[Marc L. Corliss](#), [E. Christopher Lewis](#), [Amir Roth](#)

July 2003 **LCTES '03: Proceedings of the 2003 ACM SIGPLAN conference on Language, compiler, and tool for embedded systems**

**Publisher:** ACM [Request Permissions](#)

Full text available: [Pdf](#) (291.52 KB) **Additional Information:** [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#)

**Bibliometrics:** Downloads (6 Weeks): 2, Downloads (12 Months): 11, Downloads (Overall): 225, Citation Count: 11

Code compression coupled with dynamic decompression is an important technique for both embedded and general-purpose microprocessors. *Post-fetch decompression*, in which decompression is performed after the compressed instructions have been fetched, ...

**Keywords:** DISE, code compression, code decompression

Also published in:

July 2003 **SIGPLAN Notices** Volume 38 Issue 7

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